



# MOHAMED SATHAK ENGINEERING COLLEGE

KILAKARAI-623 806, RAMANATHAPURAM DIST.

Approved by AICTE, COA, New Delhi, DGS Mumbai, Affiliated to Anna University,  
An ISO 9001:2015 Institution, Sponsored by Mohamed Sathak Trust, Chennai - 06.



( Recognized under section 2(f) & 12B of UGC, NewDelhi )

## Department of Computer Science and Engineering Innovative Teaching Methods

|   |  |
|---|--|
| Activity Title                          | Solving problems in gates  |
| Faculty Name/Department                 | Dr.V.BalaMurugan/ CSE  |
| Mapped Course Name & Code               | Microprocessor and Microcontroller EC8691  |
| Date                                    | 11-9-22  |
| Benefitted Students (Year / Sem / Dept) | III/V/CSE  |
| Topic                                   | Problems on gates  |
| Description                             | Problems related to logic gates typically involve designing and analyzing digital circuits that use these gates to perform specific functions. Logic gates are fundamental building blocks in digital electronics, and they process binary data (0s and 1s) based on Boolean logic |
| Course Outcomes (CO)                    | CO2: Understanding the Logical Gates.  |
| Performance Indicator (PI)              | 1.4.1  |
| Mail ID ( for review)                   | vbalram78@gmail.com  |

### Topics/ Questions:

1. Solving Gates by AND OR and NOT.
2. Inverse and XOR NAND.

**Marks:**

| Group Name<br>( if ITM is a<br>group<br>activity) | Reg No.             | Topic /       | Marks |      | Total |
|---|---------------------|---------------|-------|------|-------|
|   |                     |               | (10)  | (10) |       |
| A   | 5001-29             | Logical gates | 10    | 10   | 20    |
| B   | 5030-51,301-<br>312 | Logical gates | 10    | 10   | 20    |

**Outcome:**

1. Understand and execute programs based on 8086 microprocessor.
2. Design Memory Interfacing circuits.
3. Design and interface I/O circuits.